



PTO/SB/08B (07-06)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	10/764,805
Filing Date	January 28, 2004
First Named Inventor	Sang Hun OH
Art Unit	1756
Examiner Name	Sullivan, Caleen O.
Attorney Docket Number	PIA31205/ANS/US

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/CS/	BA	Eun Seok HONG; Method for Manufacturing Semiconductor Device; Korean Patent Abstracts; Publication No. 1020030000483; Publication Date: January 6, 2003; Korean Intellectual Property Office	
/CS/	BB	Byeong Hak LEE; Method for Forming Metallic Wiring of Semiconductor Device; Korean Patent Abstracts; Publication No. 1020010059181; Publication Date: July 6, 2001; Korean Intellectual Property Office	

Examiner Signature	/Caleen Sullivan/ (05/29/2007)	Date Considered	
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		Application Number	10/764,905
		Filing Date	January 26, 2004
		First Named Inventor	Sang Hun OH
		Art Unit	1756
		Examiner Name	Sullivan, Caeleen O.
		Attorney Docket Number	PIA31205/ANS/US
Sheet	1	of	1

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Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/CS/	CA	Stanley WOLF, Ph.D.; Silicon Processing for the VLSI Era, volume 4; Deep-Submicron Process Technology; 2002; Pgs. 322 - 323 and 458 - 462; Lattice Press, Sunset Beach, California	

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